

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(controller processor micro\$1controll micro\$1processor)and (ground adj fault ground\$1fault) and current adj transformer and neutral and analog adj memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/12 18:10
L2	0	((controller processor micro\$1controll micro\$1processor)and (ground adj fault ground\$1fault) and current adj transformer and neutral and analog adj memory).clm.	US-PGPUB	OR	OFF	2006/11/12 18:12
L3	5	((controller processor micro\$1controll micro\$1processor)and (ground adj fault ground\$1fault) and current adj transformer and neutral).clm.	US-PGPUB	OR	OFF	2006/11/12 18:11
L4	0	((controller processor micro\$1controll micro\$1processor)and (ground adj fault ground\$1fault) and current adj transformer and neutral and analog adj memory).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/12 18:12
L5	2	((controller processor micro\$1controll micro\$1processor)and (ground adj fault ground\$1fault) and current adj transformer and neutral and analog adj memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/12 18:21
L6	1781	(361/42 324/684 324/685).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/12 18:23
L7	8	("4542432" "4550360" "4870532" "6538863").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/12 18:31
L8	1	10/687068	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/12 18:31